General Purpose Amplifier Transistor

NPN Surface Mount

Features

- Moisture Sensitivity Level: 1
- Pb-Free Packages are Available

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	60	Vdc
Collector–Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current – Continuous	Ic	100	mAdc
Collector Current – Peak	I _{C(P)}	200	mAdc

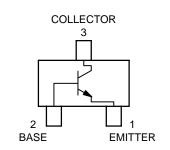
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P _D	200	mW
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



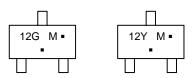
http://onsemi.com





SC-59 CASE 318D STYLE 1

MARKING DIAGRAMS



12M, 12Y = Specific Device Code

M = Date Code

■ = Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MSC2712GT1	SC-59	3000/Tape & Reel
MSC2712GT1G	SC-59 (Pb-Free)	3000/Tape & Reel
MSC2712YT1	SC-59	3000/Tape & Reel
MSC2712YT1G	SC-59 (Pb-Free)	3000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

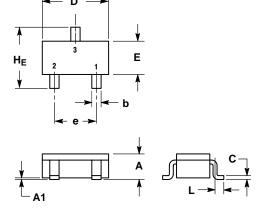
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage (I _C = 2.0 mAdc, I _B = 0)	V _{(BR)CEO}	50	-	Vdc
Collector–Base Breakdown Voltage ($I_C = 10 \mu Adc, I_E = 0$)	V _{(BR)CBO}	60	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	V _{(BR)EBO}	7.0	-	Vdc
Collector–Base Cutoff Current $(V_{CB} = 45 \text{ Vdc}, I_E = 0)$	I _{CBO}	-	0.1	μAdc
	I _{CEO}	- - -	0.1 2.0 1.0	μAdc μAdc mAdc
DC Current Gain (Note 1) $ (V_{CE} = 6.0 \text{ Vdc}, I_{C} = 2.0 \text{ mAdc}) $	h _{FE}	200 120	400 240	-
Collector–Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	-	0.5	Vdc
Current – Gain – Bandwidth Product (I _C = 1 mA, V _{CE} = 10.0 V, f = 10 MHz)	f _T	50	-	MHz

^{1.} Pulse Test: Pulse Width $\leq 300~\mu s,~D.C. \leq 2\%.$

PACKAGE DIMENSIONS

SC-59 CASE 318D-04 ISSUE G

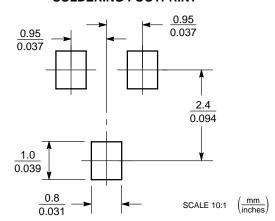


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.00	1.15	1.30	0.0394	0.0453	0.0511
A1	0.013	0.06	0.100	0.0005	0.0022	0.0040
b	0.35	0.43	0.50	0.0138	0.0167	0.0196
С	0.09	0.14	0.18	0.0034	0.0053	0.0070
D	2.70	2.90	3.10	0.1063	0.1142	0.1220
E	1.30	1.50	1.70	0.0512	0.0591	0.0669
е	1.70	1.90	2.10	0.0670	0.0748	0.0826
L	0.20	0.40	0.60	0.0079	0.0157	0.0236
HE	2.50	2.80	3.00	0.1102	0.1102	0.1181

STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

SOLDERING FOOTPRINT*



^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free LISA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.